

REMARKS

By this amendment, claims 1 and 35 have been amended. Accordingly, claims 1-38 are currently pending in the application, of which claims 1, 10, 22 and 35 are independent claims. Applicant appreciates the indication that claims 10-34 are allowed.

Entry of the Amendments and Remarks is respectfully requested because entry of Amendment places the present application in condition for allowance, or in the alternative, better form for appeal. No new matters are believed to be added by these Amendments.

In view of the above amendments and the following Remarks, Applicant respectfully requests reconsideration and timely withdrawal of the pending objections and rejections for the reasons discussed below.

Rejections Under 35 U.S.C. § 102

Claims 1-6, 8, 9 and 35-38 stand rejected under 35 U.S.C. § 102(e) as being allegedly anticipated by U. S. Patent No. 6,746,905 issued to Fukuda (“Fukuda”). Applicant respectfully traverses this rejection for at least the following reasons.

With respect to claims 1-6, 8 and 9, amended independent claim 1 recites “a *common electrode* formed in each pixel region on the substrate; a pixel electrode formed in each pixel region, *spaced apart from said common electrode with a predetermined distance therebetween*”. An example of this claimed feature is shown in Figs. 1 and 2 of the present application, in which the common electrode 271 and the pixel electrode 67 are formed on the substrate in each pixel and spaced apart from each other with a predetermined distance therebetween.

In this regard, Lee fails to show a common electrode formed in each pixel electrode. For example, in Figs. 4A and 4B, Lee shows a pixel electrode 32 but there is no common electrode. Thus, Lee fails to disclose “a pixel electrode ... *spaced apart from said common electrode with a predetermined distance therebetween*”.

Also, claim 1 recites “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”. In this regard, in Lee, as shown in Fig. 9, the channel region 12 is formed on the light shield layer 63. The layers 12 and 63 are *not formed on the same layer*. Thus, Lee fails to disclose “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”.

For these reasons, it is submitted that claim 1 is patentable over Lee. Claims 2-6, 8, 9 that are dependent from claim 1 would be also patentable at least for the same reason.

With respect to claims 35-38, independent claim 35 has been amended to further recite “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”.

In this regard, as previously mentioned, the Examiner asserted that Lee shows, in Fig. 9, the channel region 12 is formed on the light shield layer 63. The layers 12 and 63 are *not formed on the same layer*. Thus, Lee fails to disclose “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”.

For these reasons, it is submitted that claim 35 is patentable over Lee. Claims 36-38 that are dependent from claim 35 would be also patentable at least for the same reasons.

Accordingly, Applicant respectfully requests withdrawal of the 35 U.S.C. § 102(e) rejection of claims 1-6, 8, 9 and 35-38.

Rejections Under 35 U.S.C. § 103

Claim 7 stand rejected under 35 U.S.C. § 103(a) as being allegedly unpatentable over Lee in view of U. S. Patent No. 5,892,562 issued to Yamazaki, *et al.* (“Yamazaki”). Applicant respectfully traverses this rejection for at least the following reasons.

Claim 7 is dependent from claim 1. As previously mentioned, claim 1 is believed to be patentable over Lee. For example, Lee fails to disclose or suggest “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”.

In this regard, Yamazaki is directed to a liquid crystal electro-optic device, in which the gate electrode 403 and the common electrode 404 are formed on the base oxide film. However, Yamazaki does not disclose or suggest “a semiconductor layer comprising a semiconductor pattern and a light interception pattern *formed on the same layer*”. Thus, Yamazaki does not cure the deficiency from Lee.

Also, there is no motivation for combining Lee and Yamazaki. In Lee, a common electrode is not formed on the substrate on which the gate line is formed (i.e., the common electrode formed on the color substrate). In Yamazaki, the gate electrode 403 and the common electrode 404 are formed on the same substrate. Because of this structural difference, the operational mode of Lee is substantially different from that of Yamazaki. If Lee is modified such that the common electrode is form on the same substrate with the gate line, the result may render Lee’s LCD device unsatisfactory for its intended purposes. Thus, it is submitted that there is no motivation for the asserted combination.

For these reasons, it is submitted that claim 1 is patentable over Lee and Yamazaki. Claim 7 that is dependent from claim 1 would be also patentable at least for the same reasons.

Accordingly, Applicant respectfully requests withdrawal of the 35 U.S.C. § 103(a) rejection of claim 7.

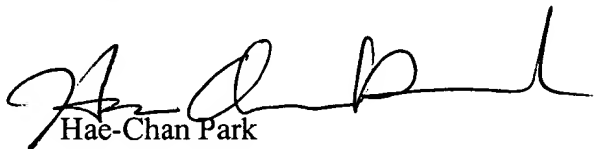
CONCLUSION

Applicant believes that a full and complete response has been made to the pending Office Action and respectfully submits that all of the stated objections and grounds for rejection have been overcome or rendered moot. Accordingly, Applicant respectfully submits that all pending claims are allowable and that the application is in condition for allowance.

Should the Examiner feel that there are any issues outstanding after consideration of this response, the Examiner is invited to contact the Applicant's undersigned representative at the number below to expedite prosecution.

Prompt and favorable consideration of this Reply is respectfully requested.

Respectfully submitted,



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